L1 Altimeter Rubric:

Schematic ___/20

Schematic includes an MCU, USB connector, storage device, and at least one sensor. ___/5

Schematic uses at least one of the following communication protocols: I2C, SPI, UART. ___/5

Schematic properly makes use of passive components such as resistors and capacitors. ___/5

Schematic is properly formatted, using text, boxes, annotations, and other tools. ___/5

Routing ___/20

Components are all properly connected in a two layer PCB layout. ___/5

Layout makes use of zones and vias. ___/5

Connections are clean and the board is understandable. ___/5

Layout is properly formatted, using edge cuts, measurements, silkscreen labels, and other formatting elements. ___/5

Soldering ___/10

All components are properly soldered onto the board. ___/5

MCU boots and sensors and storage devices respond as intended. ___/5

Total ___/50

TinkerCad 555 Timer Rubric:

Layout ___/20

The 555 Timer's pins are properly connected to the circuit.	**___/5**
The MOSFET is correctly linked to the 555 Timer, LEDs, and power.	**___/5**
The power, resistors, and capacitors are set to reasonable values.	**___/5**
Circuit is efficiently designed, with no unnecessary components or connections..	**___/5**

Functionality ___/30

When running the simulation, the LEDs are able to glow.	**___/10**
When running the simulation, the LEDs blink at regular intervals.	**___/20**

Total ___/50

KiCad 555 Timer Rubric:

Schematic ___/15

Schematic includes a 555 timer and transistor. ___/5

Schematic properly makes use of passive components such as resistors and capacitors. ___/5

Schematic is properly formatted, using text, boxes, annotations, and other tools. ___/5

Routing ___/15

Components are all properly connected in a single layer PCB layout. ___/5

Connections are clean and the board is understandable. ___/5

Layout is properly formatted, using edge cuts, measurements, silkscreen labels, and other formatting elements. ___/5

Total ___/30